# Communications & PR Review

# June 2025



# **COVERAGE HIGHLIGHTS**



## NOTV Profit

### Robust infra, faster approvals can make India a semiconductor hub

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GAINING MOMENTUM However, the country has largely due to manques invostmorms like biliomet's eries, along with the active particleation of domostic a sette including. Total and

### Amid Calls For Deregulation, Government Eases SEZ Rules For Chips, **Electronic Manufacturing**

nic components manufacturing. They would require a continuous land area of 50 hactarases

The government has relaxed land norms for special economic zones which want to exclusively set up manufacturing units for semiconductors or electronic components, according to a gazette notification

Earlier, if SEZs had to set up for chips or electronic components manufacturing, they would require a contiguous land area of 50 hectares. This has now been reduced to 10 hectares.

The notification specified that electronic components include display module sub-assembly, camera module sub-assembly, battery sub-assembly, various types of other module sub-assemblies, printed circuit board, li-ion cells for batteries, mobile and information technology hardware components,

The notification also said the condition of encumbrance-free area can also be eased in cases where the area is mortgaged or leased to the central or state government, or their authorised agency

"By liberalizing the policy and aligning it with the realities of land availability and global competitiveness. the government has reinforced India's ambition to become a global electronics manufacturing destination," said Ashok Chandak, President, India Electronics and Semiconductor Association

The latest move by the government ties in with the Chief Economic Advisor V Apartha Nageswaren's calls for deregulation as a lever for growth

In this year's Economic Survey, Napeswaran and his team wrote that regulations burt businesses' ability to start and grow over time. "Regulations also hurt workers by discouraging job creation, limiting wages and encouraging informal employment."

The survey called many current regulations 'gold-plated', overly complex and unrealistic, which assume excessive compliance capabilities.

# The Telegraph

## **Easy land norms for tech SEZs**

OUR BUREAU

Calcutta: In a bid to push dismeetic manufacturing and export of semiconductors and electronics, the Union ministry of commerce and infastries has amended the SEZ rules, easing contiguous land area norms for special aconomic sones (SEEs) that want to engage exclusively in the manufacturing of semiconductors or electronic com-

"Where a special economic some to exclusively set up for components would include the manufacturing of semi-display mobile sub-assembly. conductors or electronic comnoments. the configuous land. Mr. factory sub-assembly: area shall be 10 hectares or various types of other module more." the commerce mining sufficient try's postfordien sold. Earther. out theref. Othours on colle-Affair teas, St. havelows.

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### CHIP BOOST

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 Move will help MSMEx. and staff-ups scale upchip and electronics manufacturing

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The notification also said - competitiveness, the govern that the board of approval the ... ment has reinforced India's highest decision making body ambition to become a global for SEDs) can relat the conelectronics manufacturing dition of encumbrance-free destination," said Ashok area, in cases where the area Clundak, prevident, BISA and is mortgaged or lessed to the SEMI locks.

He added that these meanures will particularly benefit India Electronics and Septhe manufacturing of elements: ironductor Association and components such as discrete SXML the organiser of Sensisentoconductors (translators, con events including Sension diodes), integrated circuits. budge sold that those referens potential elevate boards, distillar will make it significantly susand comova modeles, battery for for compagns to establish parks and Dibtom-ion rells -facilities and scale up producmen building blocks for devices ranging from smortphones and electric vehicles to data omityes.

"This is a timely and stretegic push that strengthens the Design in India to Make in troates manufacturing and India mission, and it will belocreate new investment ninesaid that for the purposes of components, hearables and aligning it with the realities and reduce dependence on imof land availability and global ports," sold Chandak.

## THE ASIAN AGE

## Govt relaxes land norms for chip, electronics SEZs

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# businessline.

### As India pushes to become semiconductor hub, success hinges on more than incentives

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country's med chip manufactioning destination, However, experts and reaccess with depend and best on incentives, but on faster approvals, or beast indicast pursues, and a long-term focus on chip design and Walls.

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Most States have a recolocularity policy frommork, with variations in the scale and nature of financial and into colated support. The capital expecualities traperal extentily provided by status typically ranges between 26 per cent and Mi per cent of the central incentive offered podes 25M by Metry. Additionally, support for land, power, water, and housing differe form State to State, explained Autok Chandak, Freedood of DESCRIPTION OF THESE

States Sike Cojarut, Arnam, UE Odistor, and Madhra Pradesh offer comparable increasing puckages, sometimes extending semiconductor opecitic capes towerstwee above excetting industrial poticion. For instance, Assess office suggest under its prorest industrial policy as well as Shorogh targeted schemes for Semiconductors and Electronic

Consent in the majority of impariment to Francisc into Colonet, Antiqued for Access and Urter Pradicts, based on projects approved under IVM. Some states - including Orioba, Amilios Pradical, Chicaronach, and Maharashtra-have announced projects, set to occube ISM approval.

Among the States, Copulat office a 4D per over top-up-on the central capes incomitive, equating to 30 per cent of the overall project cost, UP provides a 50 per cent top-up, effectively covering half of the total project expenditure. Odisha extends a similar 30 per cent top-up-lin 15 ht approved projects and 3O per cont of project capes for others, with multiplifiers districted on a year party basis. Family Nigdly after follows that part passes model and provides a 50 per error top-up on the central capes

## THE TIMES OF INDIA

'Chinese threat' that made Dunald Trump make deal with Chine is now warrying TV, speaker, smartwatch and other electronics makers in India



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